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*Sector #1
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Docket No.: PEK-In1112

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

By:

Date: March 6, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Oliver Gehring et al.
Appl. No. : 10/045,278
Filed : January 14, 2002
Title : Method for Fabricating Embedded Nonvolatile Semiconductor
Memory Cells
Art Unit :

LETTER

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

The above-mentioned new patent application was filed on January 14, 2002 without a signed oath or declaration, under the provision of 37 C.F.R. 1.53(f).

In accordance with the above-mentioned rule, enclosed herewith is the original signed declaration.

The undersigned hereby states that the application filed in the Patent and Trademark Office is the application which the inventor(s) executed by signing the declaration. MPEP 601.01(a).

The fee required for the late filing of an oath or declaration in the amount of \$130.00 is also enclosed.

Respectfully submitted,

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GREGORY L. MAYBACK
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/mjb

Date: March 6, 2002
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